

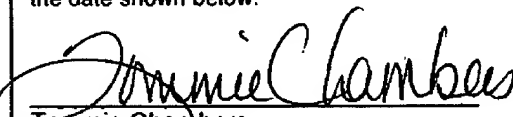
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| NAME OF INVENTOR(S): Clynes | |
| RECEIPT DATE & SERIAL NO.: Serial No.: 09/862,523 Filing Date: 5/22/2001 | |
| TITLE OF INVENTION: ON-CHIP 2D ADJUSTABLE DEFECTIVE PIXEL FILTERING FOR CMOS IMAGERS | |
| TI FILE NO.: TI-32423 | DEPOSIT ACCT. NO.: 20-0668 |
| FAXED: 8-5-06 DUE: 07/09/2005 ATTY/SECY: WDS/nc | |

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|-------------------|---|-------------------|----------------------|
| Applicant: | Clynes | Docket No: | TI-32423 |
| Serial No: | 09/862,523 | Examiner: | Moe, Aung Soe |
| Filed: | 5/22/2001 | Art Unit: | 2612 |
| For: | ON-CHIP 2D ADJUSTABLE DEFECTIVE PIXEL FILTERING FOR CMOS IMAGERS | | |

Assistant Commissioner For Patents
Washington, DC 20231

Dear Sir:

Responsive to the Office Action mailed May 9, 2005 in connection with the above identified application, Applicants respectfully submit the following amendments and remarks.

PAGE 2/9 * RCVD AT 8/5/2005 12:41:31 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-6/25 * DNIS:2738300 * CSID:972 917 4417 * DURATION (mm-ss):02:12